

FIG. 1

PRIOR ART

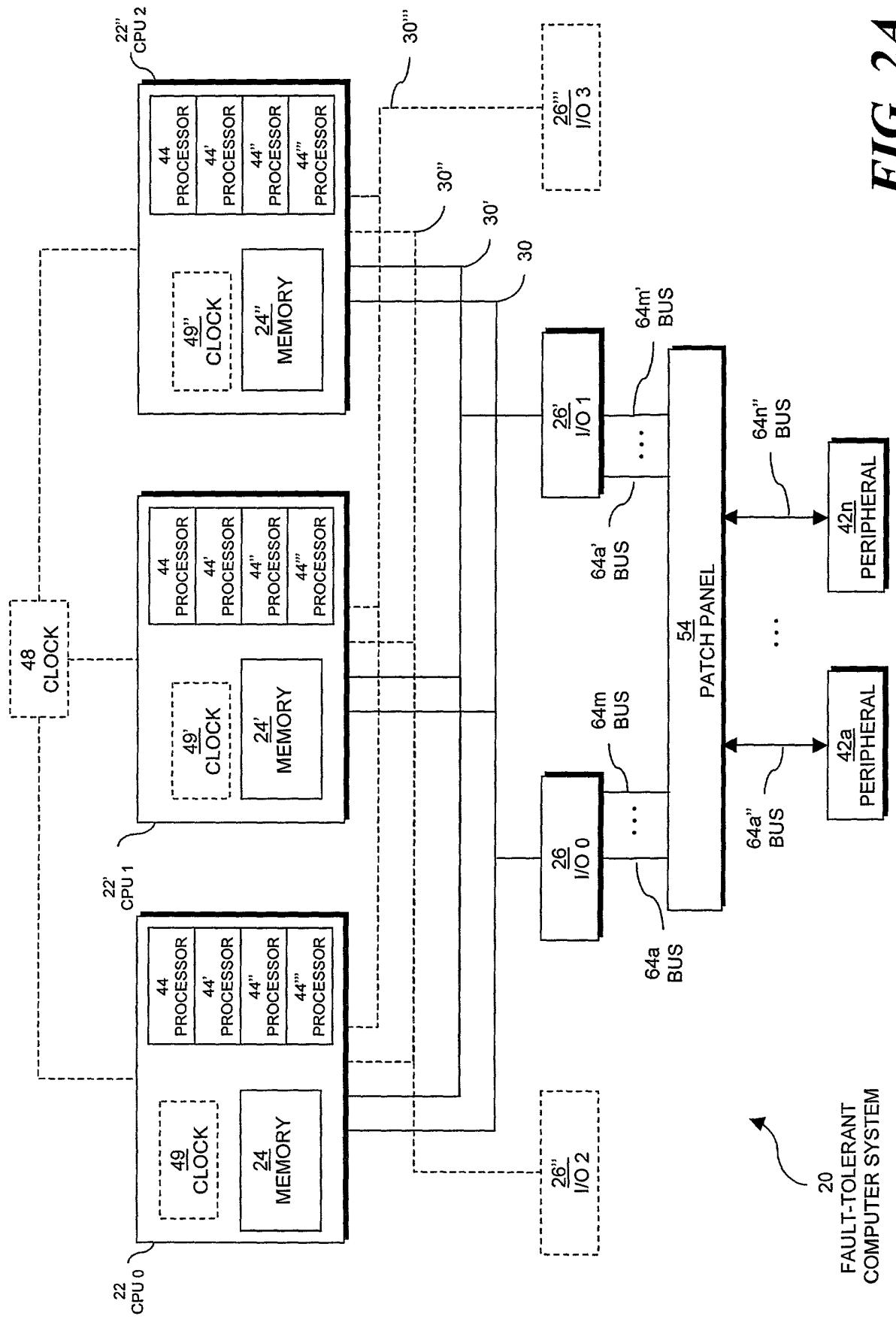


FIG. 2A

20 COMPUTER SYSTEM

56

COMMUNICATIONS LINK

30

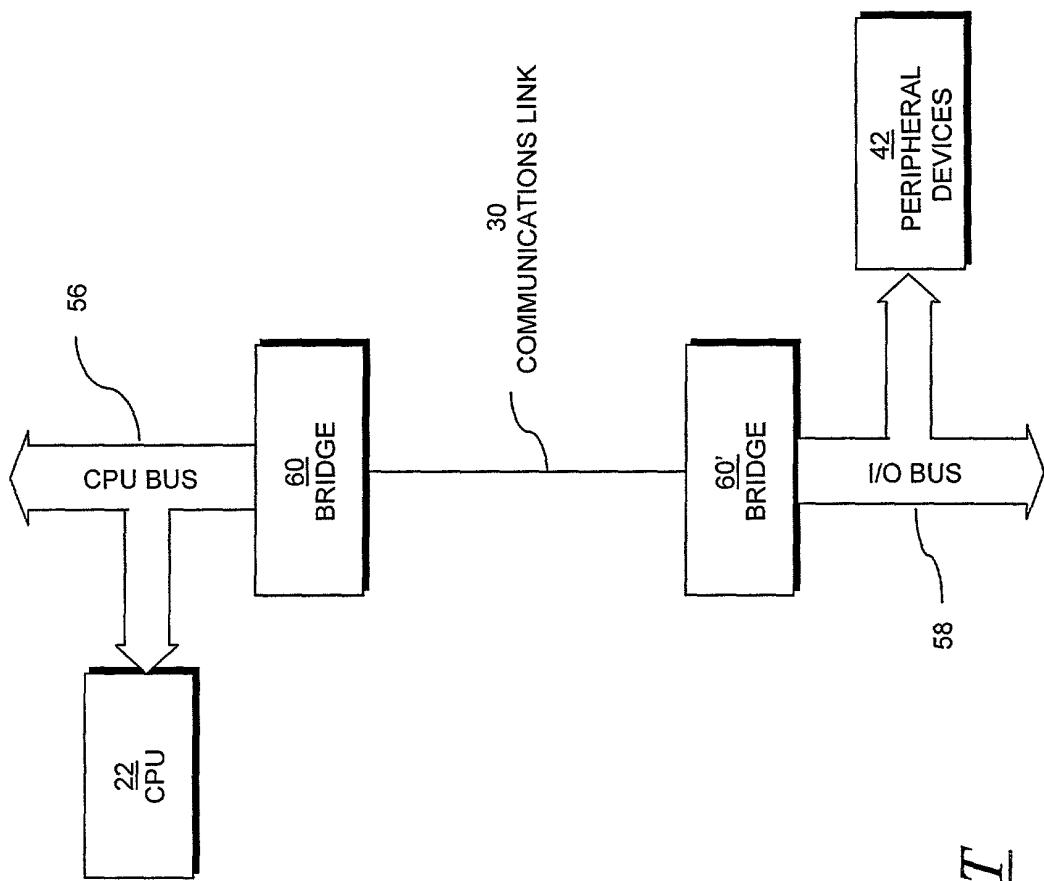
60 BRIDGE

60' BRIDGE

42 PERIPHERAL DEVICES

58 I/O BUS

22 CPU



PRIOR ART

FIG. 2B

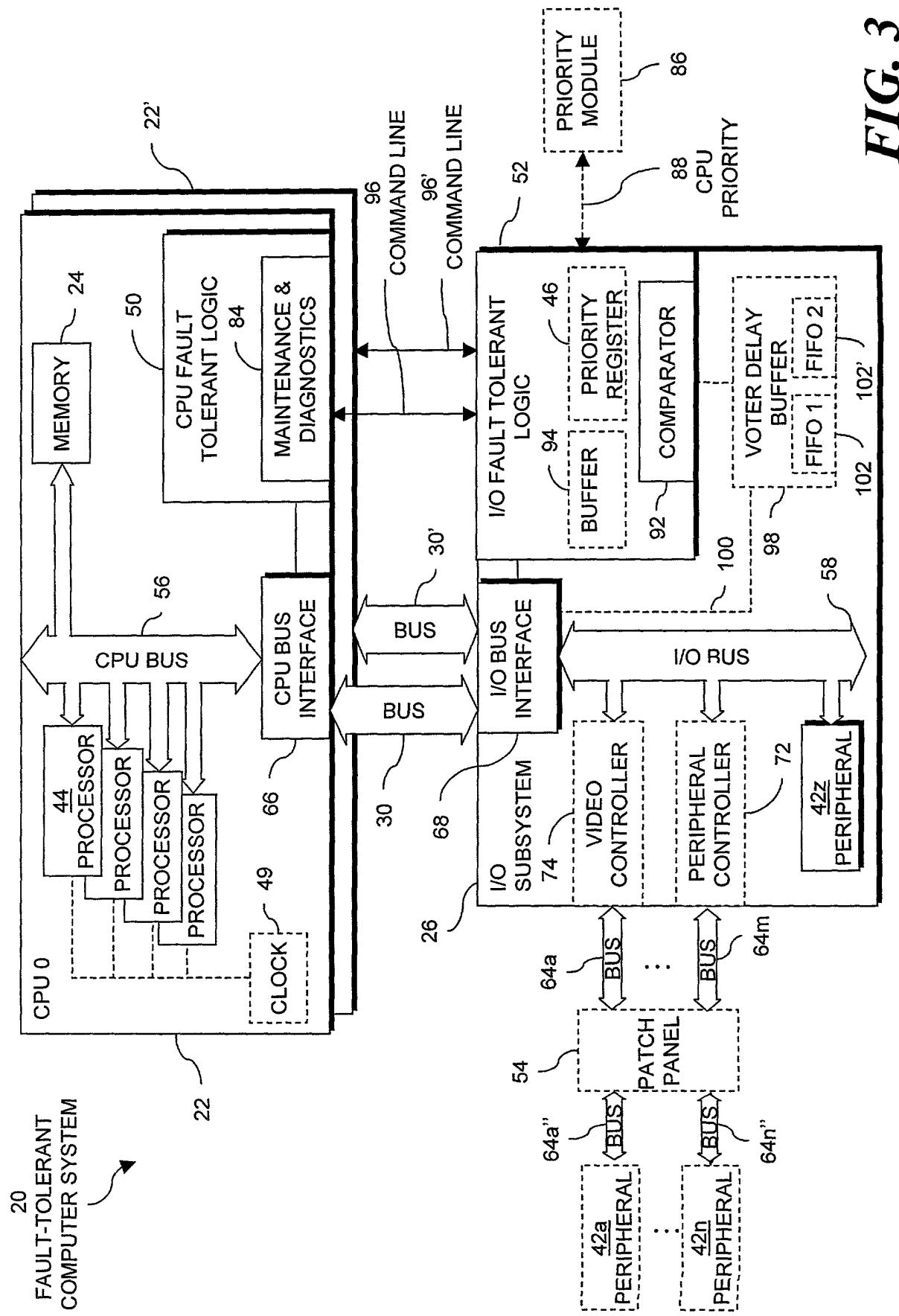


FIG. 3

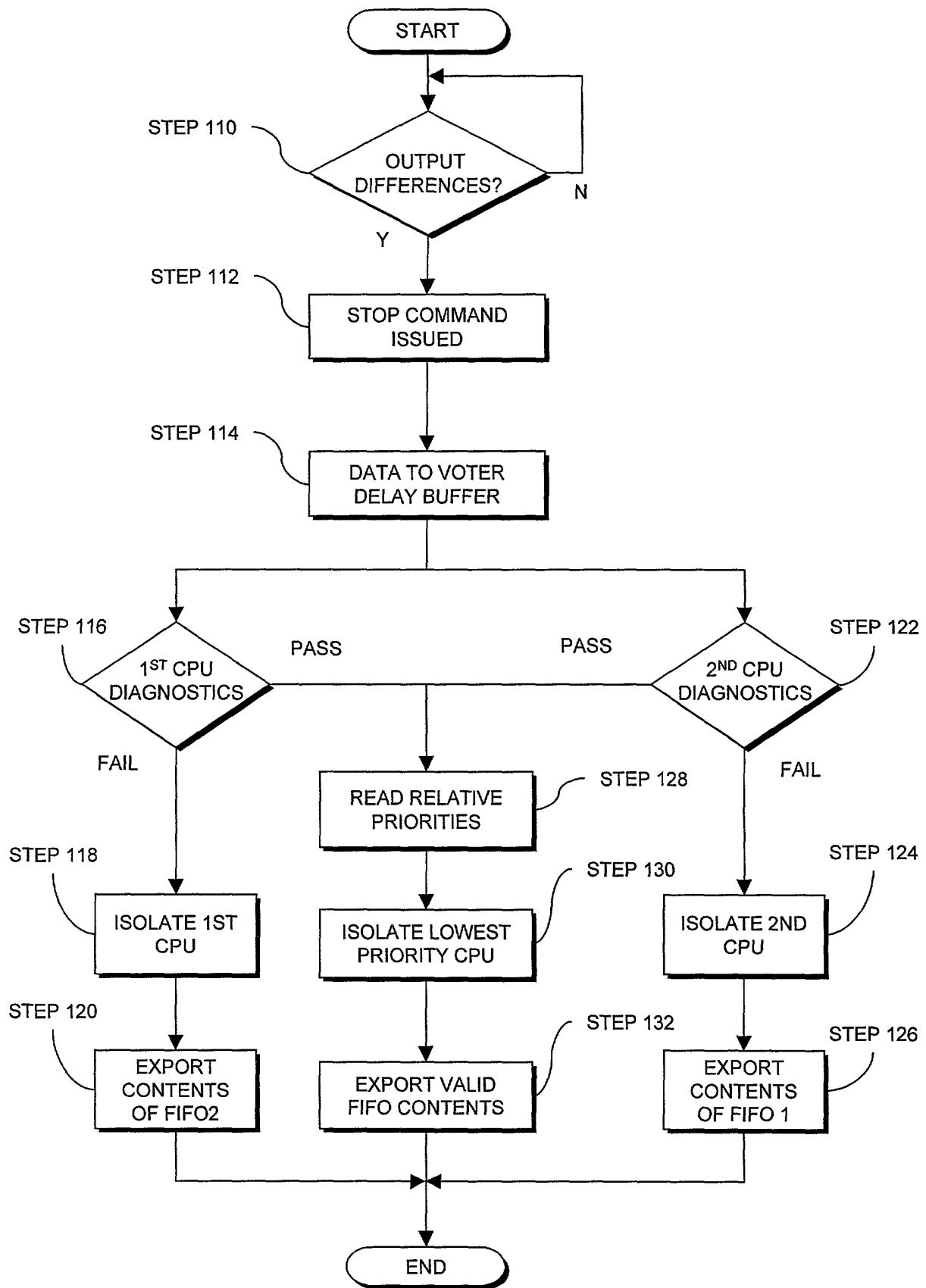


FIG. 4

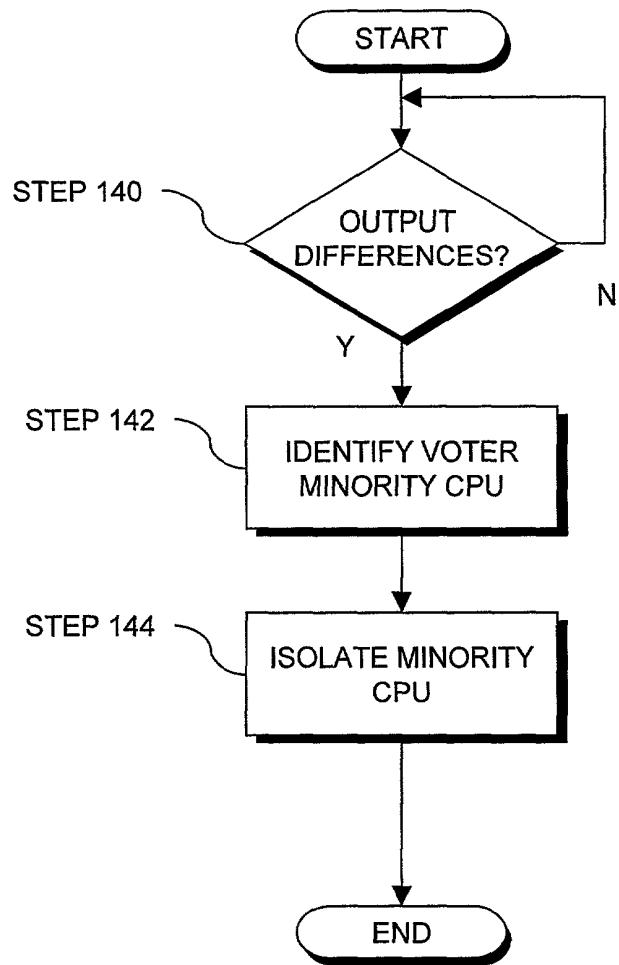


FIG. 5

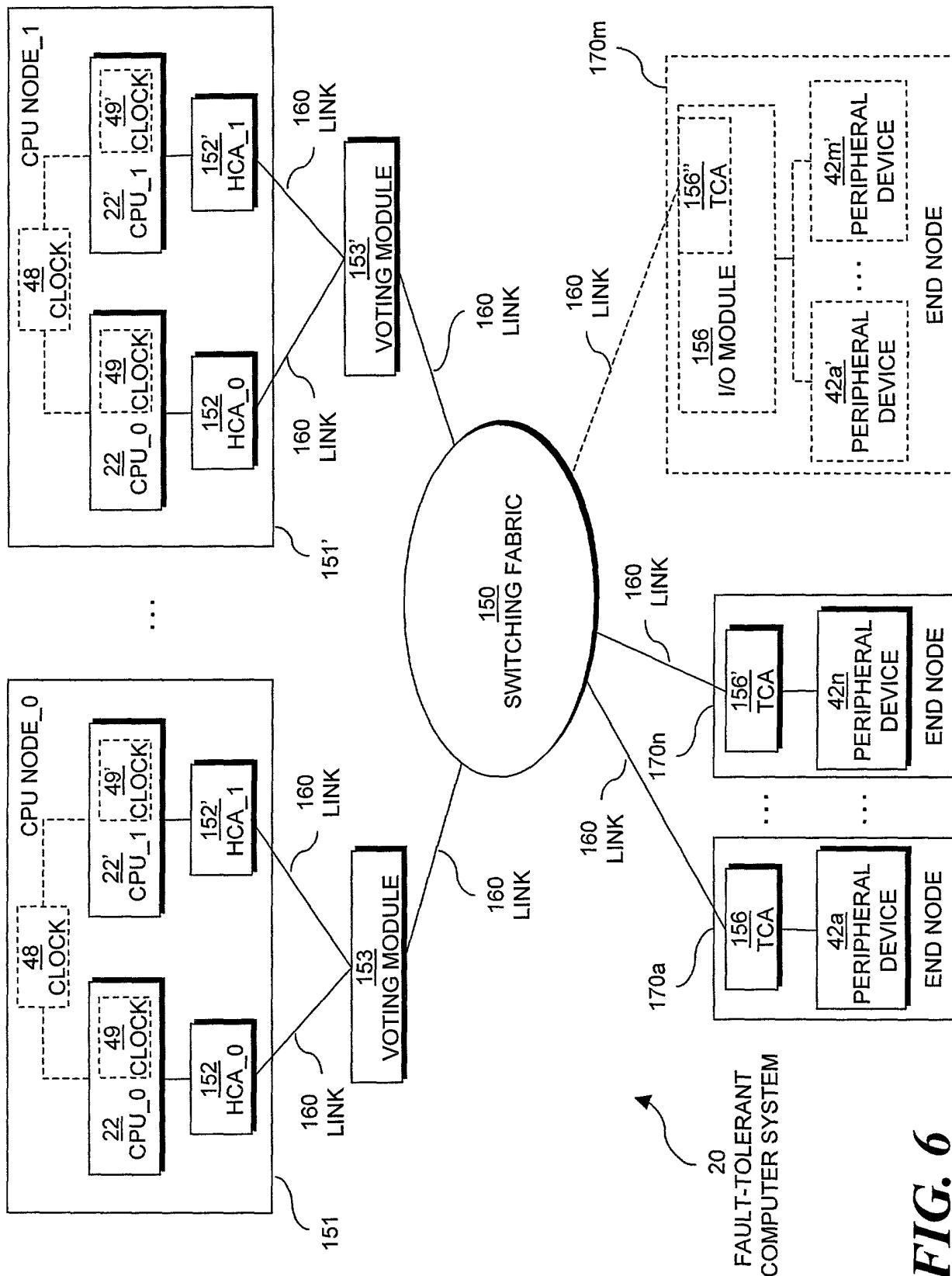


FIG. 6

